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20.4 A 56.4-to-63.4GHz Spurious-Free All-Digital Fractional-N PLL in 65nm CMOS

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13.3 A 50mW-TX 65mW-RX 60GHz 4-Element Phased-Array Transceiver with Integrated Antennas in 65nm CMOS

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19.2 A Digitally Modulated 2.4GHz WLAN Transmitter with Integrated Phase Path and Dynamic Load Modulation in 65nm CMOS

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13.2 A Digitally Modulated mm-Wave Cartesian Beamforming Transmitter with Quadrature Spatial Combining

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2.2 A 66Gb/s 46mW 3-Tap Decision-Feedback Equalizer in 65nm CMOS

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25.3 A Self-Duty-Cycled and Synchronized UWB Receiver SoC Consuming 375pJ/b for -76.5dBm Sensitivity at 2Mb/s

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13.4 A Low-Power Radio Chipset in 40nm LP CMOS with Beamforming for 60GHz High-Data-Rate Wireless Communication

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